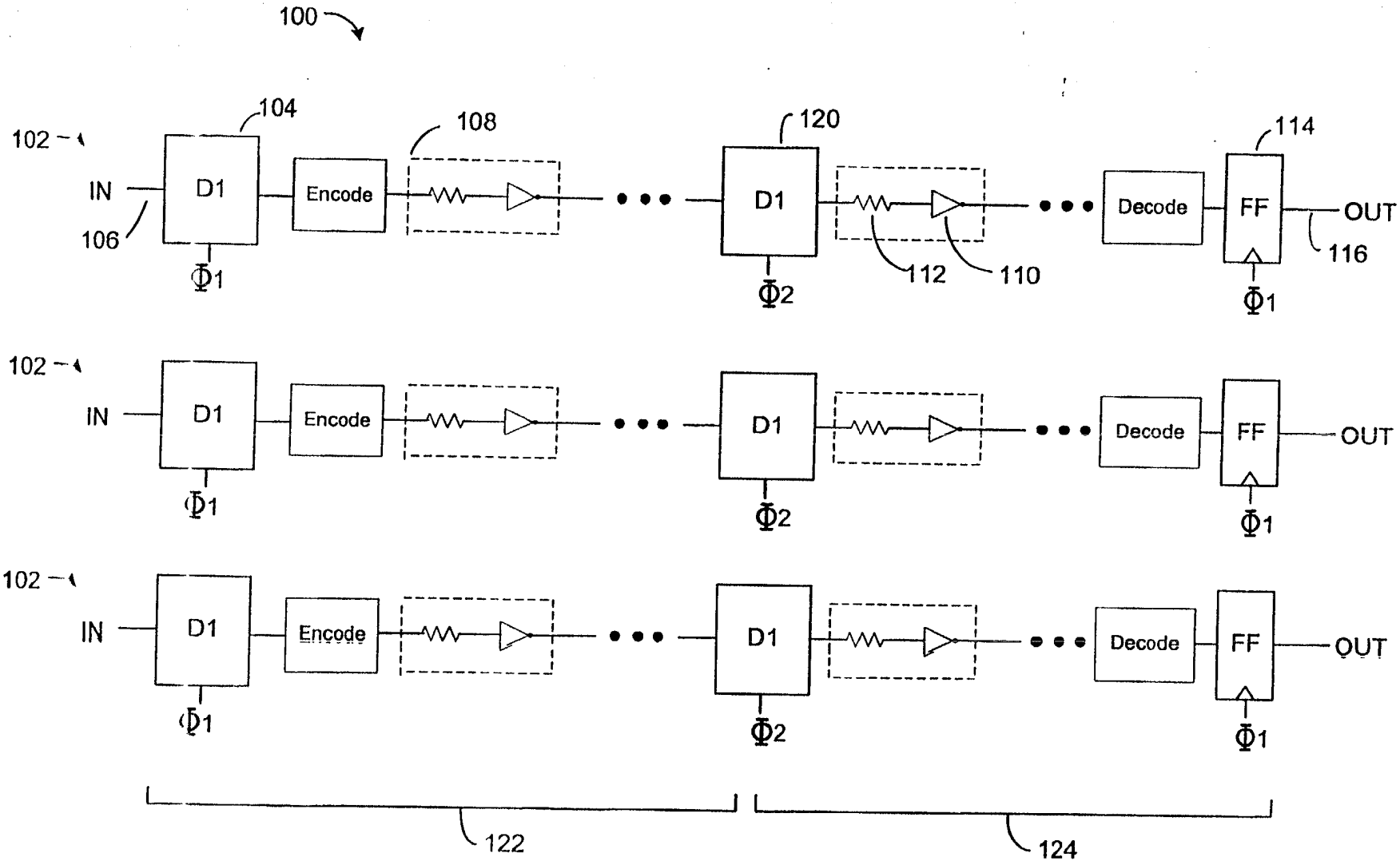
100
102
104
108
120
114
IN
106
D1
Encode
D1
Decode
FF
OUT
116
112
110
Φ1
Φ2
Φ1
122
124

FIG. 1

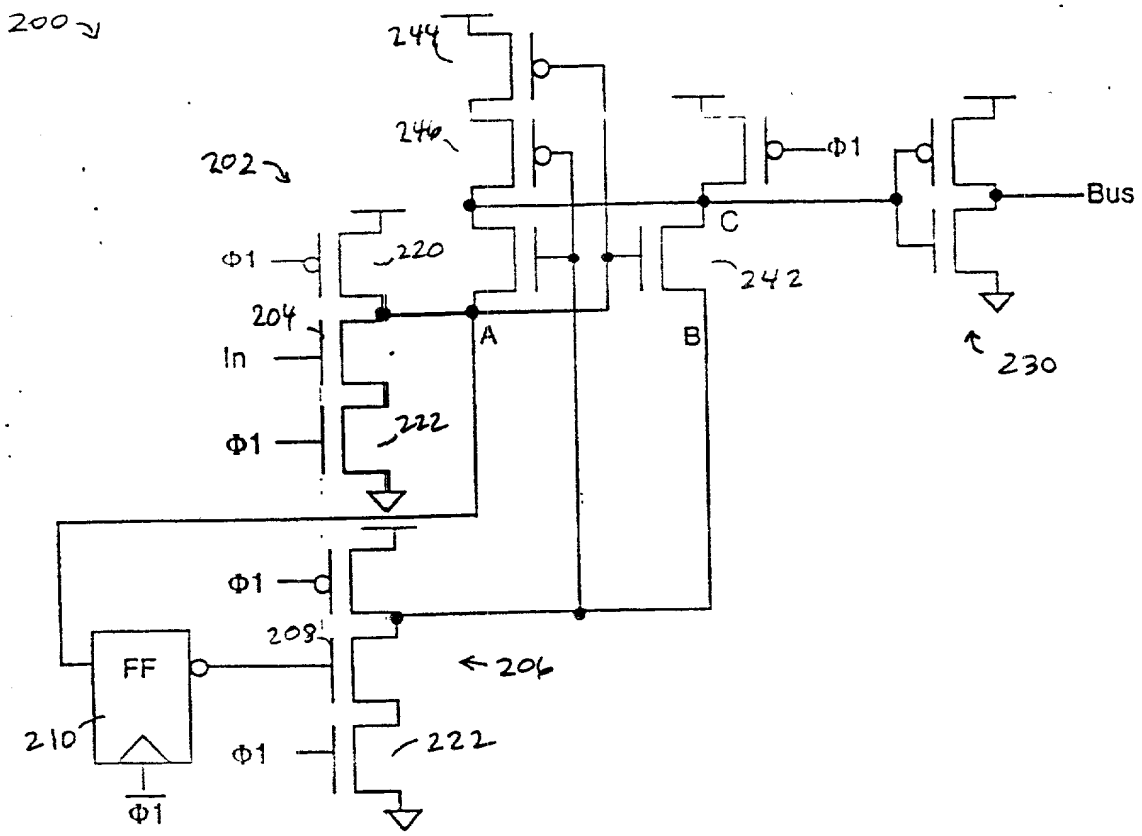
200
244
240
202
Φ1
Bus
C
Φ1
220
242
204
In
A
B
230
Φ1
222
Φ1
208
206
FF
210
Φ1
222
Φ1

FIG. 2

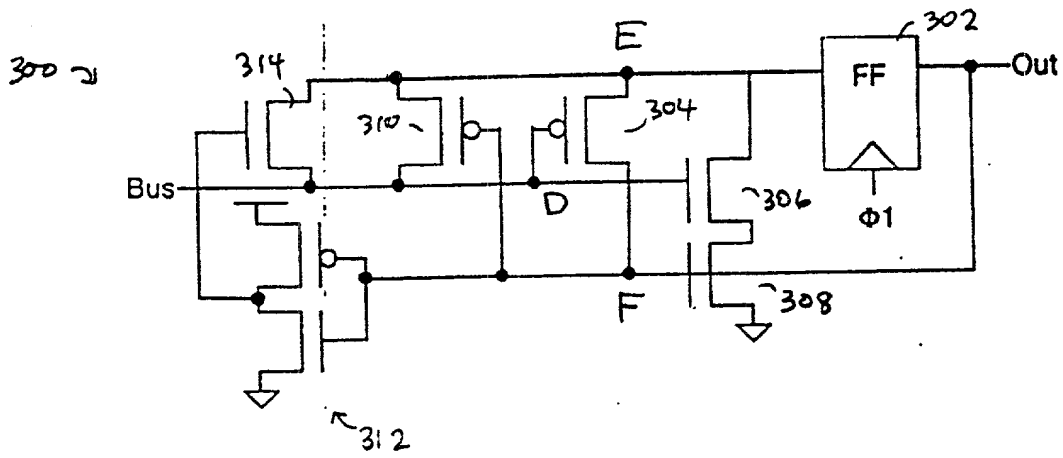
300
314
E
302
FF
Out
310
304
Bus
D
306
Φ1
F
308
312

FIG. 3